# EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|---|---|---|---|---|---|---|
| L1 | 0 | (system with antenna near3 tuning near3 circuit with tuning near3 coil and receiver with double near3 conversion near3 variable near3 capacitance near3 diode and high near3 frequency near3 amplification with signal near5 antenna near3 tuning and first near3 second near3 mixing and detecting near3 circuit with processing near3 output with second near3 mixing).clm. | US-PGPUB | OR | ON | 2007/04/30 12:56 |
| L4 | 1 *10/533,777 this application* | (digital near3 analog near3 converter with generating near3 control near3 voltage with setting near3 tuning near3 frequency with variable near3 capacitance near3 diode with antenna near3 tuning near3 circuit and local near3 oscillator with frequency near3 variable with first near3 mixing near3 circuit with amplification and control near3 section near3 setting near3 frequency with local oscillator and generating near3 frequency near3 setting near3 associating near3 tuning near3 frequency near5 antenna with frequency near3 local near3 oscillation with inputting near3 frequency near3 setting near3 data near3 digital near3 analog near3 converter).clm. | US-PGPUB | OR | ON | 2007/04/30 13:03 |
| L5 | 0 | (wherein near3 digital near3 analog near3 converter near3 comprises with temperature near3 coefficient near3 setting near3 section with elements near3 having near3 predetermined near3 temperature near3 coefficients and wherein near3 device near3 constant near3 temperature near3 coefficient near3 setting near3 section near3 as near3 whole near3 changed near3 accordance with ambient near3 temperature).clm. | US-PGPUB | OR | ON | 2007/04/30 13:01 |

# EAST Search History

| L6 | 0 | (wherein near3 digital near3 analog near3 converter with high near3 frequency near3 amplification with first near3 second near3 mixing with detecting with local near3 oscillator with formed near3 "same" near3 semiconductor near3 substrate and wherein near3 temperature near3 coefficient near3 setting with plurality near3 resistances with formed near3 semiconductor near3 manufacturing near3 process with temperature near3 coefficients near3 different near3 each near3 other and wherein near3 connection near3 form near3 plurality near3 resistances near3 set with temperature near3 coefficient near3 digital near3 analog near3 converter with predetermined near3 value).clm. | US-PGPUB | OR | ON | 2007/04/30 13:02 |
|---|---|---|---|---|---|---|
| L7 | 0 | 1 and 4 and 5 and 6 | US-PGPUB | OR | ON | 2007/04/30 13:02 |